

BCA - II SEM**DIGITAL ELECTRONICS (CODE: 106)**

| S. NO. | NAME OF STUDENT | ENROLL. NO. | TOPICS |
|--------|---------------------|-------------|--|
| 1 | PRATHAM KHANNA | 00190102019 | Logic Gates : Not, And , Or |
| 2 | JATIN | 00290102019 | Universal Gates : Nand Nor, Ex-Or & Ex-Nor Gates |
| 3 | PRAGYA BHARGAVA | 00390102019 | Diode & Transistor |
| 4 | MOHIT SINGH SLATHIA | 00490102019 | Logic Families: Rtl , Dtl Ttl Ecl Cmos |
| 5 | ABHISHEK KUMAR | 00590102019 | Definition Of Current & Volatge Parameters, Noise Margin , Fan -In , Fan-Out |
| 6 | NEERAJ BHARDWAJ | 00690102019 | Boolean Algebra Basics Laws |
| 7 | ASHISH KUMAR TANWAR | 00790102019 | Simplification Of Boolean Equations Using K - Maps |
| 8 | MOHIT | 00890102019 | Reviews Of Various Number Systems (Binary , Octal, Hexadecimal) |
| 9 | ADITYA SINGH | 00990102019 | Bcd |
| 10 | ASHWINI RAI | 01090102019 | Gray Codes & Excess- 3 Codes & Their Application |
| 11 | ANURAG VERMA | 01190102019 | Parity Generation & Checking |
| 12 | UMA | 01290102019 | Adder |
| 13 | SUMIT SHOKEEN | 01390102019 | Subtractor |
| 14 | DEEPAK KUMAR VERMA | 01490102019 | Parallel Binary Adder/ Subtarctor |
| 15 | ANJALI PANDEY | 01590102019 | Binary Multiplier |
| 16 | SATYAM BHATT | 01690102019 | Divider |
| 17 | AAKASH | 01790102019 | Multiplexer & Demultiplexer |
| 18 | GAURAV KUMAR | 01890102019 | Decoders & Encoders |
| 19 | CATHERINE JEENA | 01990102019 | Flip- Flops: S-R |
| 20 | RIDDHESH KOHLI | 02090102019 | J-K Flip Flop |
| 21 | JAYANT TAKWAL | 02190102019 | T & D Flip Flop |
| 22 | PRIYANSH GAUTAM | 02290102019 | Race Around Condition |
| 23 | SAURABH | 02390102019 | Master Slave Flip Flop |
| 24 | NAMAN BADHAN | 02490102019 | Realisation Of One Flip Flop Using Other Flip Flop |
| 25 | NITISH JAIN | 02590102019 | Shift Registers: Sipo, Pipo, Piso, Siso |
| 26 | SURESH SINGH SAUD | 02690102019 | Bi-Directional Register |
| 27 | YASH KAUSHIK | 02790102019 | Counters: Ripple Counter |
| 28 | YASHIKA YADAV | 02890102019 | Synchronous Counter |
| 29 | PAWAN GUSAIN | 02990102019 | Modulo Counter |

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|--------|-----------------------|-------------|--|
| 30 | DEEPAK KUMAR | 03090102019 | Ring Counter |
| 31 | SAGAR KARHANA | 03190102019 | Twisted Ring |
| 32 | JATIN JARYAL | 03290102019 | Memory Devices: Ram & Rom |
| 33 | KARTIK SINGH RANA | 03390102019 | Pal & Pla |
| 34 | AKANKSHA | 03490102019 | Logic Gates : Not, And , Or |
| 35 | RAMAN GANGWANI | 03590102019 | Universal Gates : Nand Nor, Ex-Or & Ex-Nor Gates |
| 36 | SUMIT DHABAL DEV | 03690102019 | Diode & Transistor |
| 37 | NEHA | 03790102019 | Logic Families: Rtl , Dtl Ttl Ecl Cmos |
| 38 | CHAVI DWIVEDI | 03890102019 | Definition Of Current & Volatge Parameters, Noise Margin , Fan -In , Fan-Out |
| 39 | YAMINI | 03990102019 | Boolean Algebra Basics Laws |
| 40 | MRIDUL ANAND | 04190102019 | Simplification Of Boolean Equations Using K - Maps |
| 41 | HARSH | 04290102019 | Reviews Of Various Number Systems (Binary , Octal, Hexadecimal) |
| 42 | KRUSHNA CHANDRA MAHAP | 04390102019 | Definition Of Bcd |
| 43 | GAUTAM GUPTA | 04490102019 | Gray Codes & Excess- 3 Codes & Their Application |
| 44 | VIKASH TIWARI | 04590102019 | Parity Generation & Checking |
| 45 | UDITANSHU SHARMA | 04690102019 | Adder |
| 46 | ANKIT KUMAR JHA | 04790102019 | Subtractor |
| 47 | DEEPAK KUMAR SINGH | 04890102019 | Parallel Binary Addder/ Subtarctor |
| 48 | ROHAN RAI | 04990102019 | Binary Multiplier |
| 49 | NAVEEN SHARMA | 05090102019 | Divider |